



FlacheSAN4N24M-UN

Key Features

- Storage server supports Single Socket AMD EPYC CPU up to 2TB DDR4
- Portable tower with 24x M.2 or NF1 Drives (Rack-mount option available)
- Supports 6x full height PCIe3 slots + 1x PCIe3 OCP
- Ability to power on/off M.2/NF1 drives individually



Specifications

Processor Support	Supports Single Socket AMD EPYC (Naples) CPU up to 180W TDP socket SP3	Supported OS	Windows 2016 64bit, Linux RHEL 7.3, 7.4, 7.5 x64, SUSE 11 SP4 x64, SUSE 12 SP2 x64, Ubuntu 16.04 x64, Ubuntu 17.04 x64
I/O Interface	2x USB 3.0 ports; 1x VGA port(s); 1x COM Port; 2x 1GbE ports; 1x 1GbE MGMT	Front Panel	Power On/Off with LED, Reset Switch, NMI Switch, Locate Switch with LED, 4xLAN LED, Warning LED
Memory Support	Supports up to 16x DIMMs DDR4 RDIMM/RDIMM/LRDIMM 2666/2400 MHz	Cooling	(6) 60x25mm fans or (4) 60x38mm + (2) 60x25mm
Expansion Slot	(2) PCIe3 x16 FH/FL + (2) PCIe3 x8 FH/HL + (1) PCIe3 x16 OCP 2.0 or (6) PCIe3 x8 FH/HL + (1) PCIe3 x8 OCP 2.0	Other Features	Dedicated GbE for IPMI 2.0
Drive Bays	24x hot-swap M.2 or NF1 NVMe SSD drive bays 2x internal 2.5" for OS drive bays	Weight	Net Weight (with SSD): 38lbs; Gross Weight: 55lbs
Network	2x GbE ports, 1x GbE dedicated for IPMI	Dimension	System: 16"x7"x13.5" (LxWxH) Packaging: 24"x 12"x18"(LxWxH)
Power	1+1 800W or 1600W AC/DC 80Plus Platinum redundant power supplies; Optional -48VDC available	Logistic	HTS Code: 8473 30 5100; ECCN: 4A994
		Environmental	Operating Temperature: 0°C to 35°C Non-Operating Temperature: -20°C to 70°C Humidity: 5% to 95% non-condensing
		Compliance	CE, FCC Class A, RoHS 6/6 compliant

Ordering Information

BB424NMSATY16-1	FLACHESAN4N24M-UN EPYC UP Server 24bay M.2 800W RPSU Rev. A
BB424NMSATY1A-1	FLACHESAN4N24M-UN EPYC UP Server 24bay M.2 1600W RPSU Rev. A
BB424NNSATY16-1	FLACHESAN4N24N-UN EPYC UP Server 24bay NF1 800W RPSU Rev. A
BB424NNSATY1A-1	FLACHESAN4N24N-UN EPYC UP Server 24bay NF1 1600W RPSU Rev. A

- **Autonomous Vehicle**
- **Military and Surveillance**
- **Portable Data Migration**
- **Media Entertainment Production**